

CONCLUSION

Because no new matter has been added, Applicants respectfully submit that the above referenced patent application is entitled to the original filing date of June 30, 2000.

Please telephone the undersigned at (512) 794-3600 if there are any questions.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on June 15, 2001.

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6/15/01
Date of Signature

Respectfully submitted,

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ATTACHMENT B

In the following, insertions are bold typeface and underlined, and deletions are in bold typeface and enclosed in brackets.

The paragraph starting on page 1, line 4 is amended as follows.

This application claims the benefit of provisional Patent Application No. **[60/***,***]** **60/211,815**, filed June 15, 2000.

The paragraph starting on page 1, line 18 is amended as follows.

This application is related to Patent Application No. [] **09/608,097** having attorney docket no. M-8321 US filed June 30, 2000, and entitled "CONCATENATION DETECTION ACROSS MULTIPLE CHIPS," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.

The paragraph starting on page 2, line 1 is amended as follows.

This application is related to Patent Application No. [] **09/607,912** having attorney docket no. M-8320 US filed June 30, 2000, and entitled "PATH AIS INSERTION FOR CONCATENATED PAYLOADS ACROSS MULTIPLE CHIPS," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.

The paragraph starting on page 2, line 8 is amended as follows.

This application is related to Patent Application No. [] **09/608,461** having attorney docket no. M-8340 US filed June 30, 2000, and entitled "CHANNEL ORDERING FOR COMMUNICATION SIGNALS SPLIT FOR MATRIX SWITCHING," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.

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